

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Inventor
1	US 6348719 B1	20020219	12	Using a change in doping of poly gate to permit placing both high voltage and low voltage transistors on the same chip	257/412	257/392	Chapman, Richard A.
2	US 6265739 B1	20010724	36	Non-volatile semiconductor memory device and its manufacturing method	257/296	257/392; 257/908	Yaegashi, Toshitake et al.
3	US 6201274 B1	20010313	9	Semiconductor device with no step between well regions	257/314	257/315; 257/316; 257/392	Kanamori, Kohji
4	US 6198140 B1	20010306	15	Semiconductor device including several transistors and method of manufacturing the same	257/392	257/403; 257/406; 257/500	Muramoto, Hidetoshi et al.
5	US 5600164 A	19970204	8	Non-volatile semiconductor memory device	257/321	257/392	Ajika, Natsuo et al.

392 + non volatile or non-v

Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09/960,399 on June 24, 2002

Combined Classifications

2 110/245  
2 110/257  
2 110/258  
2 110/263  
2 422/58  
2 424/450  
2 424/489  
2 424/490  
2 424/491  
2 424/492  
2 424/493  
2 424/497  
2 428/422  
2 435/6  
2 435/7.23